EFS ID:	9939060				
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Application Number:	10620045				
International Application Number:					
Confirmation Number:	7529				
Title of Invention:	METHOD AND APPARATUS FOR AUTOMATIC GENERATION OF MULTIPLE INTEGRATED CIRCUIT SIMULATION CONFIGURATION				
First Named Inventor/Applicant Name:	Rajarshi Bhattacharya				
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Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /₊zip	Pages (if appl.)
1	Request for Certificate of Correction 1150-1145 REQ.	1150-1145_REQ.pdf	179027	no	3
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National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.